

**In the Specification**

Please replace the paragraph beginning on page 3, line 5 with the following amended paragraph:

In accordance with an aspect of the present invention, there is provided a microcontroller, comprising: a clock input pin, wherein an input signal from the external circuit is inputted; a clock generating means for generating a clock signal by receiving a signal from the clock input pin; a clock output pin for receiving an output signal of the clock generating means and outputting the output signal; a first switch for transmitting an internal signal of a microcontroller to the clock output pin for using the clock output pin is in a system mode; and a second switch, which is enabled when the clock generating means is operated in the clock generation mode and disabled when the microcontroller is operated in the system mode.

Please replace the paragraph beginning on page 3, line 18 with the following amended paragraph:

In accordance with another aspect of the present invention, there is provided a system having a microcontroller, comprising: a clock input pin for receiving an input signal; a first clock generating means for receiving a signal from the clock input pin to generate a clock signal; a first switch for transmitting an internal signal of a microcontroller to the clock output pin for using the clock output pin is in a system mode; a second switch, which is enabled when the clock generating means is operated in the clock generation mode and disabled when the microcontroller is operated in the system mode; and a second clock generating means for providing a clock signal to the microcontroller through the clock input pin in a system mode.

Please replace the paragraph beginning on page 5, line 5 with the following amended paragraph:

Referring to FIG. 2, a microcontroller 40, also known as MCU chip, is built in the MDS and a separate clock generator ~~200~~ 100 is included outside of the microcontroller 40 to provide a clock to the microcontroller 40.

Please replace the paragraph beginning on page 5, line 26 with the following amended paragraph:

Thus, the present invention includes the separate clock generator 100 and the clock output pin XTAL\_out in the microcontroller 40 is selectively used in the System mode and the ~~clock~~ clock generation mode. Therefore, the microcontroller 40 having the built-in internal clock generator 200 can be loaded into the special equipment, such as the MDS equipment, so that the data output pin is not required.

Please replace the paragraph beginning on page 7, line 18 with the following amended paragraph:

At this time, if the clock applied to the microcontroller or the MDS system is below ~~to~~ the maximum operating frequency, the noise in the unnecessary frequency bandwidth is also amplified.